



Attorney Docket No. 0756-7719

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/581,674

Filed: June 5, 2006

For: METHOD FOR MANUFACTURING
THIN FILM INTEGRATED CIRCUIT
DEVICE, NONCONTACT THIN FILM
INTEGRATED CIRCUIT DEVICE
AND METHOD FOR
MANUFACTURING THE SAME, AND
IDTAG AND COIN INCLUDING THE
NONCONTACT THIN FILM
INTEGRATED CIRCUIT

) Group Art Unit: 2823

) Examiner: William D. Coleman

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
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) 2009.

Adeline M. Stampler

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 C.F.R. 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

This Information Disclosure Statement is being submitted with an RCE. Therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,



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